

Axial Solid Tantalum Capacitor – JTG

FEATURES

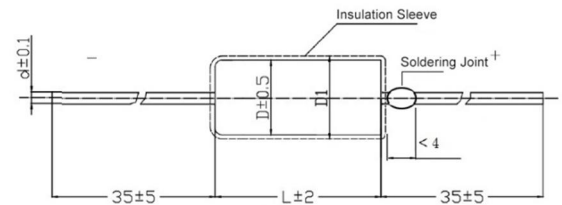
- Metal case encapsulation, Hermetically-sealed, Tubular, Axial-lead, With insulation sleeve, Heteropolarity.
- Stable in Electrical Characteristics, High reliability, Good Storage Stability Long life-span, Low DF & DCL.
- Applying in Telecommunications, such Electrical Equipments with DC & impulse Circuit.

SPECIFICATIONS

Technical Data	All technical data relate to an ambient temperature of +25°C
Capacitance Tolerance	±20%, ±10%,
Temperature Range	-55°C to +125°C
DC Leakage	$I_o \leq 0.01C_R U_R$ (μA) or 0.5μA (which is greater)



DIMENSIONS					
Case Code	Uninsulated		With Insulated Sleeve		d±0.1 (mm)
	D±0.5 (mm)	L±2 (mm)	D1 max (mm)	L max (mm)	
A	3.2	8	4	10	0.4
B	5	12	5.8	14	0.6
C	6	14	6.8	16	0.6
D	8	14	8.8	16	0.8
E	8	22	8.8	24	0.8



Temperature Characteristics									
Capacitance Range C_R (μF)	Range of Capacitance (%)			DF (%)				DCL (μA)	
	-55°C	85°C	125°C	-55°C	25°C	85°C	125°C	85°C	125°C
≤1	±8	±8	±12	3	3	3	3	8I _o	10I _o
1.5 ~ 68				5	5	5	5		
100 ~ 330				6	6	6	6		
470 ~ 1000				8	8	8	8		

Rated Voltage, Category Voltage, and Nominal Capacitance									
Rated Voltage U_R (V)	6.3	10	16	25	35	40	63	75	100
Category Voltage U_R (V)	4	6.3	10	16	20	25	40	50	63
Case Code	Nominal Capacitance C_R (μF)								
A	4.7	3.3	2.2	1	0.68	0.47	0.22	0.22	0.1
	6.8	4.7	3.3	1.5	1	0.68	0.33	0.33	0.15
	10	6.8	4.7	2.2	1.5	1	0.47	0.47	0.22
	15	10	6.8	3.3	2.2	1.5	0.68	0.68	0.33
	22	15	10	4.7	3.3	2.2	1	--	0.47
B	--	--	--	6.8	--	--	--	--	0.68
	33	22	15	10	4.7	3.3	1.5	1	1
	47	33	22	15	6.8	4.7	2.2	1.5	1.5
	68	47	33	22	10	6.8	3.3	2.2	2.2
C	100	68	47	33	15	10	4.7	3.3	--
	150	100	68	47	22	15	6.8	4.7	3.3
D	220	150	100	68	33	22	10	6.8	4.7
	330	220	150	100	47	33	15	10	--
E	470	330	220	--	68	47	22	--	--
	680	470	330	150	100	68	33	--	--
	1000	680	470	220	150	100	47	--	--

- Note:
1. Please do not use multimeter through the measuring procedures.
 2. Capacitance and DF measured at 100Hz, $U_- = 2.20^{0.10}V$, $U_+ = 1.0^{0.05}V$, Frequency=100Hz. Test only applied in series equivalent circuit.
 3. Voltage derating is applied at +125°C. (The DCL parameter should be read after 5 minutes when it connected to the circuit).
 4. Special size and demand could consult with us.

Please visit our website to get more update data, those data & specification are subject to change without notice.